Riphah

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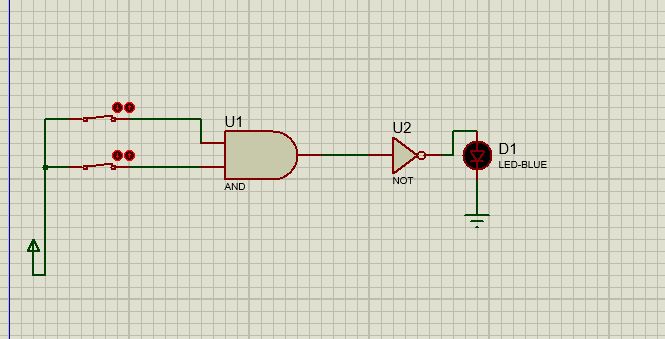
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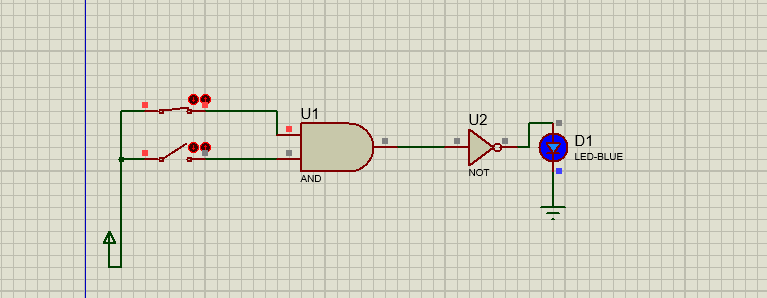
lab 3

computer architecture

**Task 1**

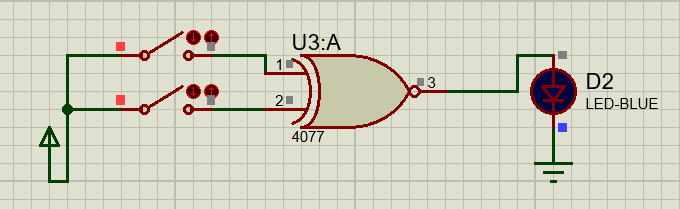
* 1. Design and simulate a simple NAND Gate using switches instead of logic states.

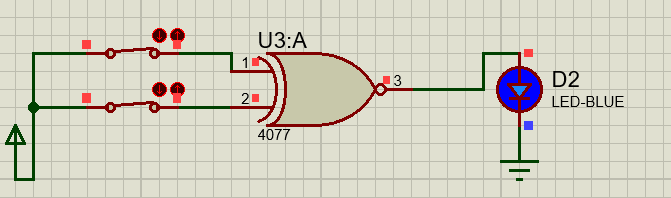


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**Task 2**

Design and simulate XNOR Gate.





**Task 3**

Design and simulate a half Subtractor.

